Examiner-Initiated Interview Summary	Application No.	Applicant(s)
	10/650,298	DESALET AL.
	Examiner	Art Unit
	GERMAN VIANA DI PRISCO	2617
All Participants:	Status of Application: <u>Am</u> <u>Rejection</u>	endment after Non-Hinal
(1) <u>GERMAN VIANA DI PRISCO</u> .	(3)	
(2) <u>MIKE DESANCTIS</u> .	(4)	
Date of Interview: 24 March 2010	Time: <u>2:41 PM</u>	
Type of Interview:  ☐ Telephonic ☐ Video Conference ☐ Personal (Copy given to: ☐ Applicant ☐ Applicant's representative)  Exhibit Shown or Demonstrated: ☐ Yes If Yes, provide a brief description:  Part I.		
Rejection(s) discussed: Potential 35 U.S.C. 101 and 112 1 <sup>st</sup> paragraph		
Claims discussed: New claim 34		
Prior art documents discussed: None		
Part II.		
SUBSTANCE OF INTERVIEW DESCRIBING THE GENERAL NATURE OF WHAT WAS DISCUSSED: See Continuation Sheet		
Part III.		
<ul> <li>It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview directly resulted in the allowance of the application. The examiner will provide a written summary of the substance of the interview in the Notice of Allowability.</li> <li>It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview did not result in resolution of all issues. A brief summary by the examiner appears in Part II above.</li> </ul>		
/Germán Viana Di Prisco/ Examiner, Art Unit 2617	Applicant/Applicant's Representat	ive Signature – if appropriate)

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Continuation of Substance of Interview including description of the general nature of what was discussed: The Examiner asked Mr. DeSanctis via e-mail on 3/22/2010." with regard to newly added claim 34, outly out direct me where in the specification is there support for the claimed "program storage device readable by a network device"? Mr. DeSanctis responded on 3/23/2010. "Examples of program storage devices in the specification include volatile memory of the netmods (see paragraphs [0018] and [0022]) and the memory of the processing resources (see paragraph [0019]), both of which are indicated to be capable of storing software instructions. For example, processing resource 130 of FIG. 1 is said to include a bridging application 132.